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## (30)Priority

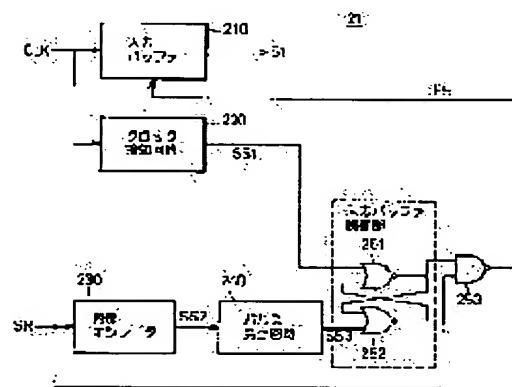
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## (54) INPUT CIRCUIT AND SEMICONDUCTOR INTEGRATED CIRCUIT HAVING THE SAME

## (57)Abstract:

**PROBLEM TO BE SOLVED:** To conduct the lock-on process of a clock by a DLL circuit in a semiconductor integrated circuit (for example, a synchronous DRAM) after the completion of a self refresh without the necessity of a dummy cycle and in a short time.

**SOLUTION:** The circuit is provided with an input buffer 210, which receives an inputted external control signal CLK and outputs an internal control signal S1, and a control signal detecting circuit 220 which detects whether the signal CLK is operating or not. The buffer 210 outputs the signal S1 if the signal CLK is operating by the output of the circuit 220.



## LEGAL STATUS

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